

NOTES, UNLESS OTHERWISE SPECIFIED:

1. RESISTANCE VALUES ARE IN OHMS.
2. CAPACITANCE VALUES ARE IN MICROFARADS.
3. INDUCTANCE VALUES ARE IN MICROHENRIES.
4. HIGHEST REFERENCE DESIGNATOR USED:

- | | |
|-------------------------|--------|
| A. CERAMIC CAPS | C119 |
| B. TANTALUM CAPS | CT20 |
| C. INDUCTORS | L7 |
| D. RESISTORS | R125 |
| E. FILTERS | E1 |
| F. DIODES/RECTIFIERS | D9/CR5 |
| G. TRANSISTORS | Q2 |
| H. IC'S | U31 |
| I. CRYSTALS/OSCILLATORS | Y2 |
| J. CONNECTORS/HEADERS | J7 |
| K. SWITCHES | SW2 |
| L. TEST POINTS | TP14 |

5. PARTS NOT INSTALLED ARE INDICATED WITH 'NU'
THE FOLLOWING IS THE LIST OF UNINSTALLED PARTS:

C36,C37,C38,C39,C40,C41,C101,C102,C105

CT11

E1

R10,R15,R27,R28,R30,R82,R86,R88,
R93,R96,R98,R101,R103,R106,R109

U4,U5

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

PRELIMINARY (GREEN PWB)

CHANGES FROM RED PWB:

- SHT13 - Shifted signals down one pin on each side of J3. This change results in pins 1 and 2 not connected (the DSK doesn't supply 12V).
- SHT16 - Replaced J4 with a schematic part that more closely represents the mechanics of the part used for J4.
- SHT17 - Added a short between U1 pins 9 and 10.

SHEET

- 01: COVER SHEET
- 02: PARALLEL PORT I/F
- 03: JTAG
- 04: HPI-C6211
- 05: CLOCK-PLL & PRG LOGIC
- 06: CLOCK-OSCILLATORS
- 07: EMIF-C6211
- 08: EMIF-SDRAM(16M-BIT X2)
- 09: EMIF-SDRAM(64M-BIT X2) (FOR FUTURE USE)
- 10: EMIF-ROM & I/O PORT
- 11: DB-DATA XCVRS
- 12: DB-ADDR & CNTRL XCVRS
- 13: DB-PERIPH & MEM I/F
- 14: ANALOG I/F CIRCUIT
- 15: MCBSP-C6211
- 16: PWR-INPUT, SEQ, & SVS
- 17: PWR- 1.8V REGULATOR
- 18: PWR- 3.3V REGULATOR
- 19: PWR-DECOUPLING C6211
- 20: PWR-DECOUPLING (SHEETS 2-5)
- 21: PWR-DECOUPLING (SHEETS 6-9)
- 22: PWR-DECOUPLING (SHEETS 10-16)

REVISION STATUS OF SHEETS

REV	*						
SH	22						
REV	*	*	*	*	*	*	*
SH	15	16	17	18	19	20	21
REV	*	*	*	*	*	*	*
SH	8	9	10	11	12	13	14
REV	*	*	*	*	*	*	*
SH	1	2	3	4	5	6	7

DWN	DATE
G. Connelly	
CHK	DATE
M. Dawkins	
ENGR	DATE
G. Connelly	
ENGR-MGR	DATE
M. Dunn	
QA	DATE
MFG	DATE
RLSE	DATE

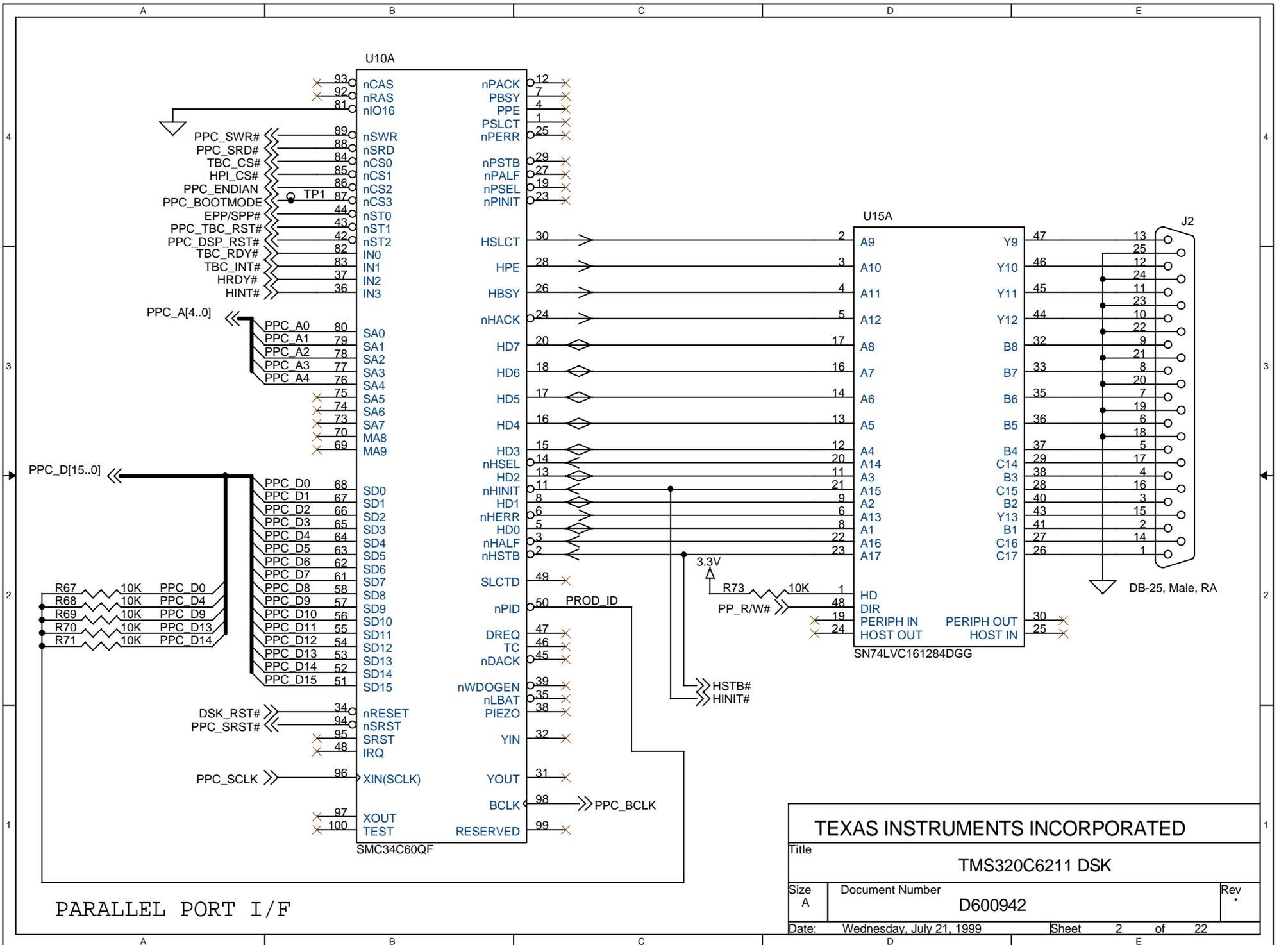
TEXAS INSTRUMENTS INCORPORATED

Software Development Systems, Semiconductor Group, Houston, Texas

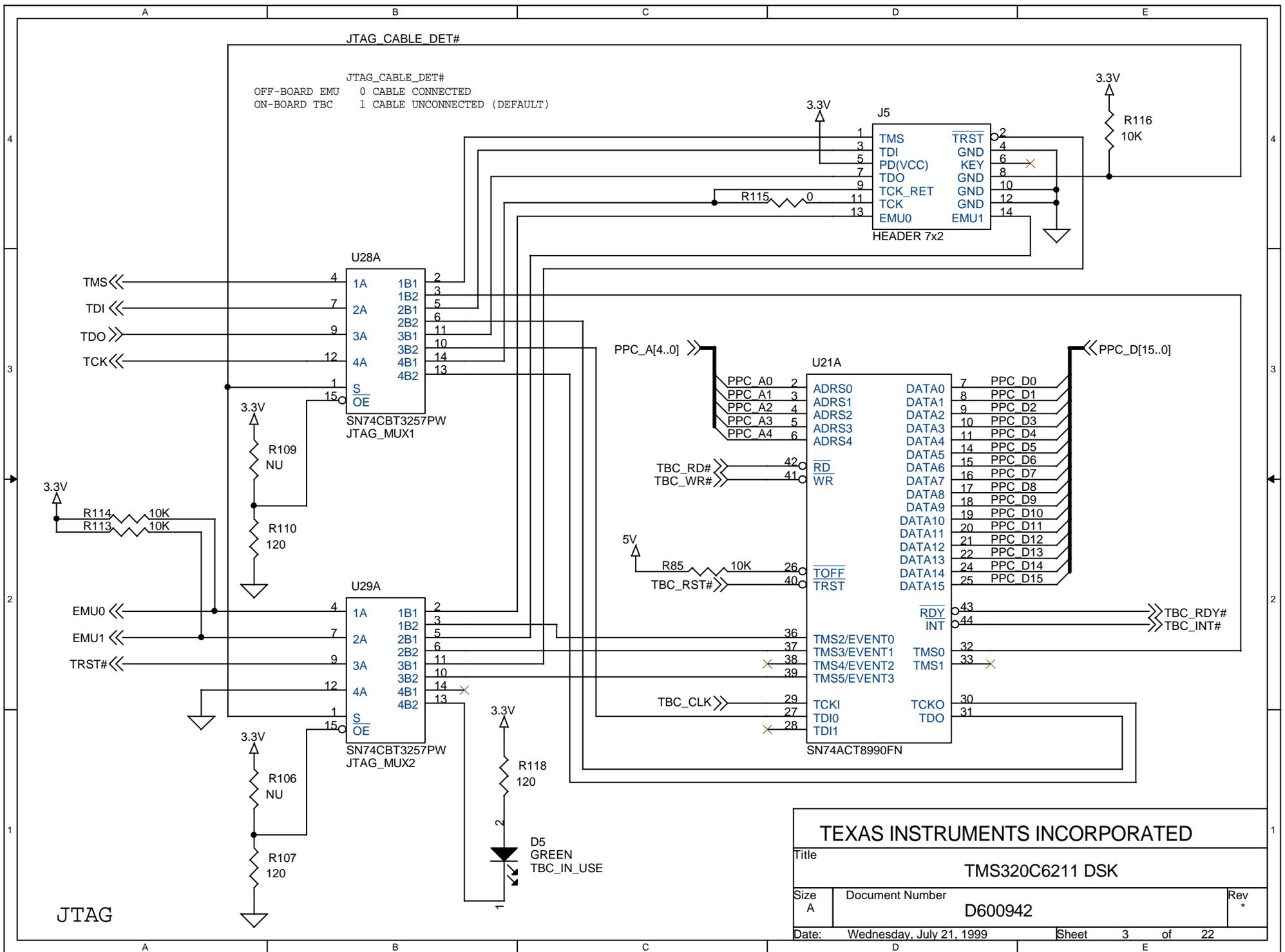
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Size	Document Number	Rev	*
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A

B

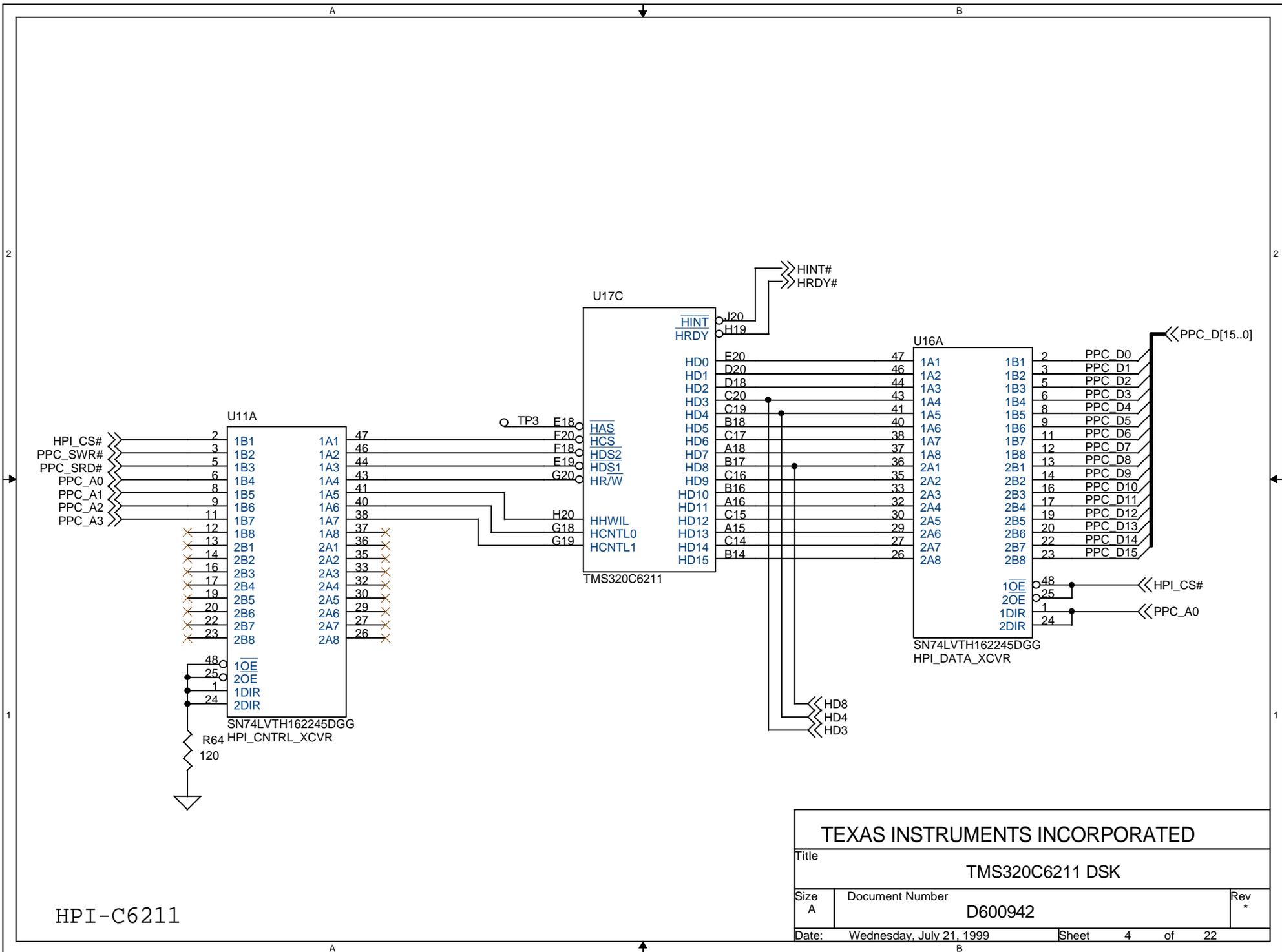


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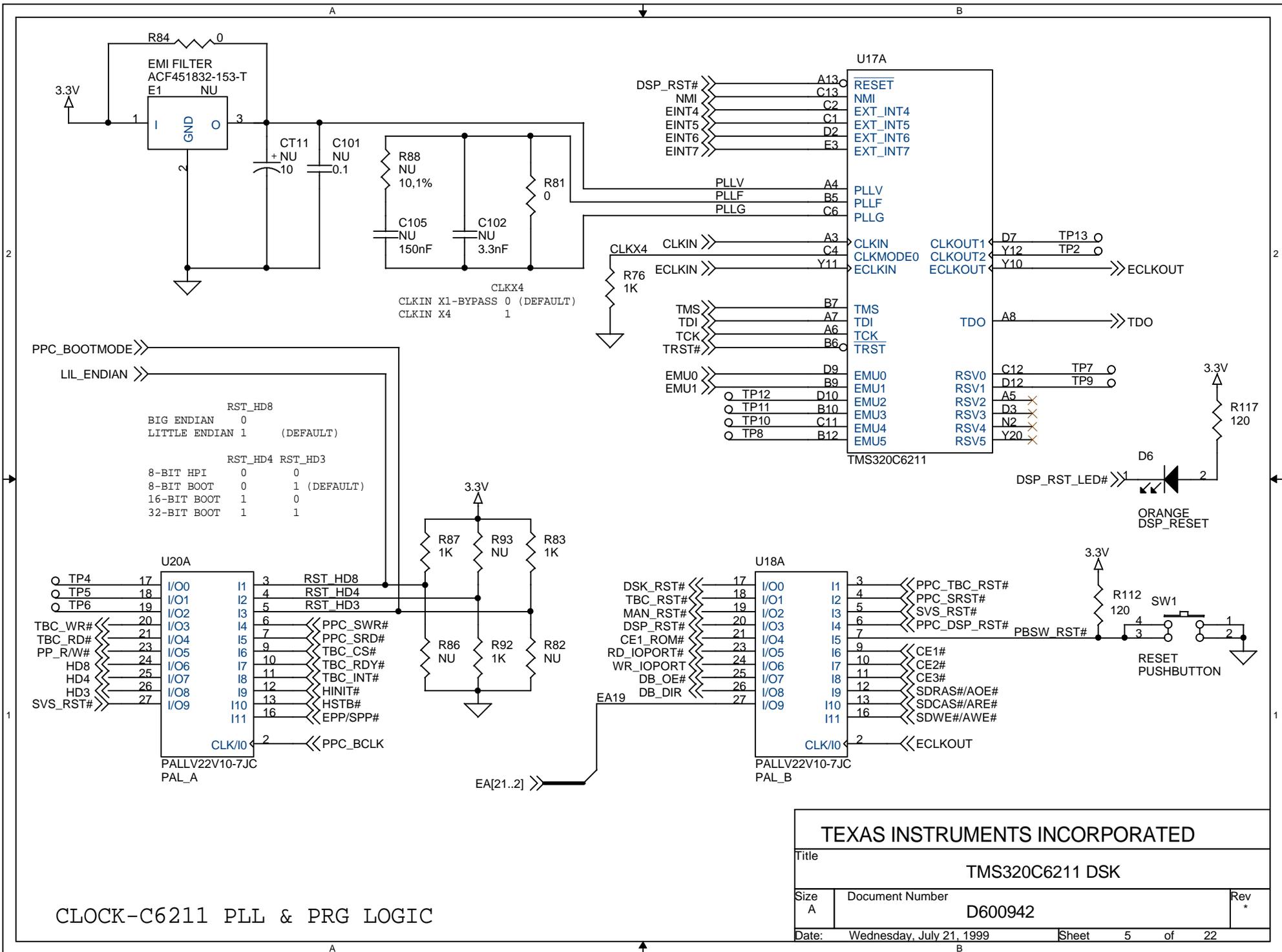
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JTAG



HPI-C6211

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CLOCK-C6211 PLL & PRG LOGIC

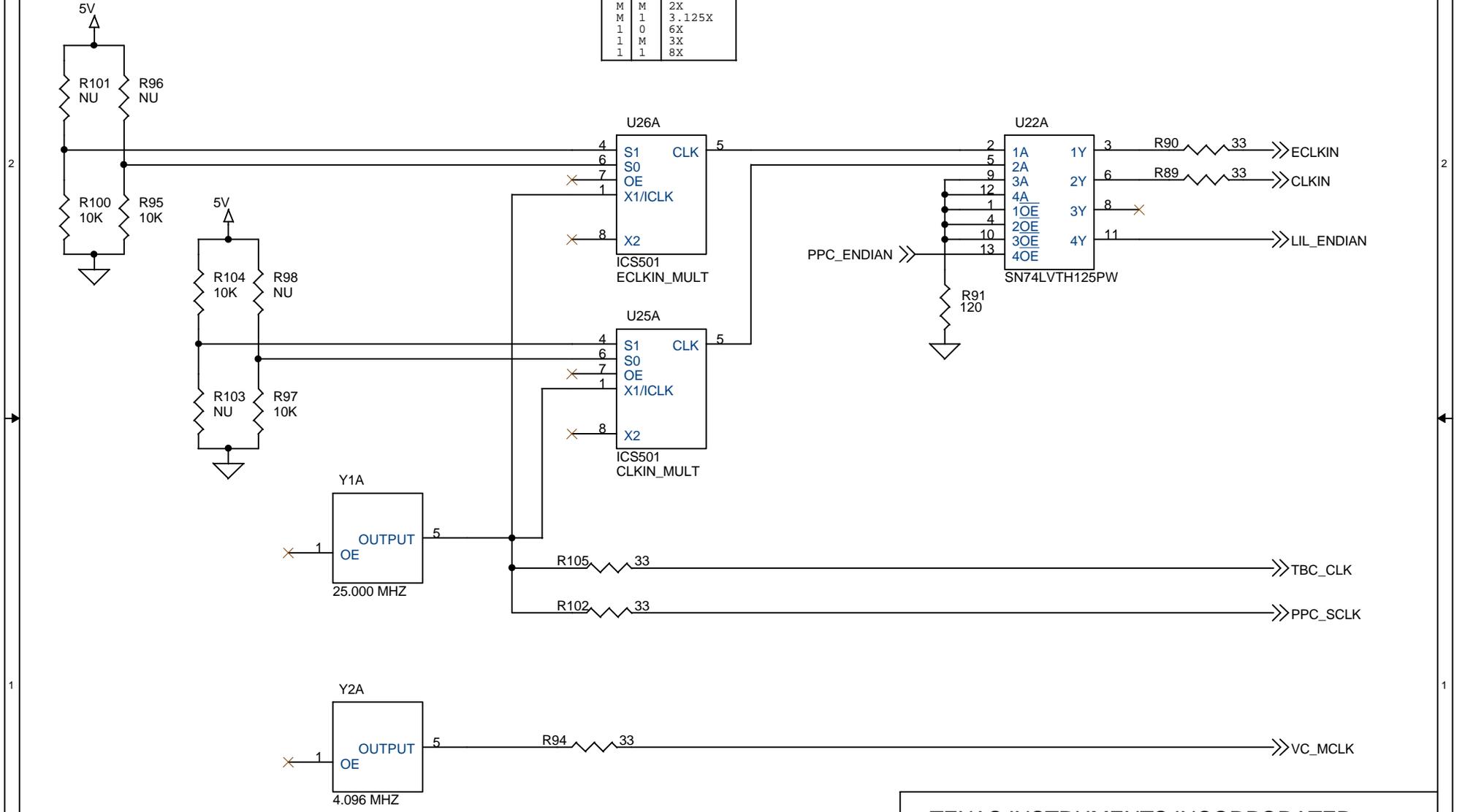
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A

B

ICS501 SELECTION TABLE

S1	S0	CLK
0	0	4X
0	M	5.3125X
0	1	5X
M	0	6.25X
M	M	2X
M	1	3.125X
1	0	6X
1	M	3X
1	1	8X

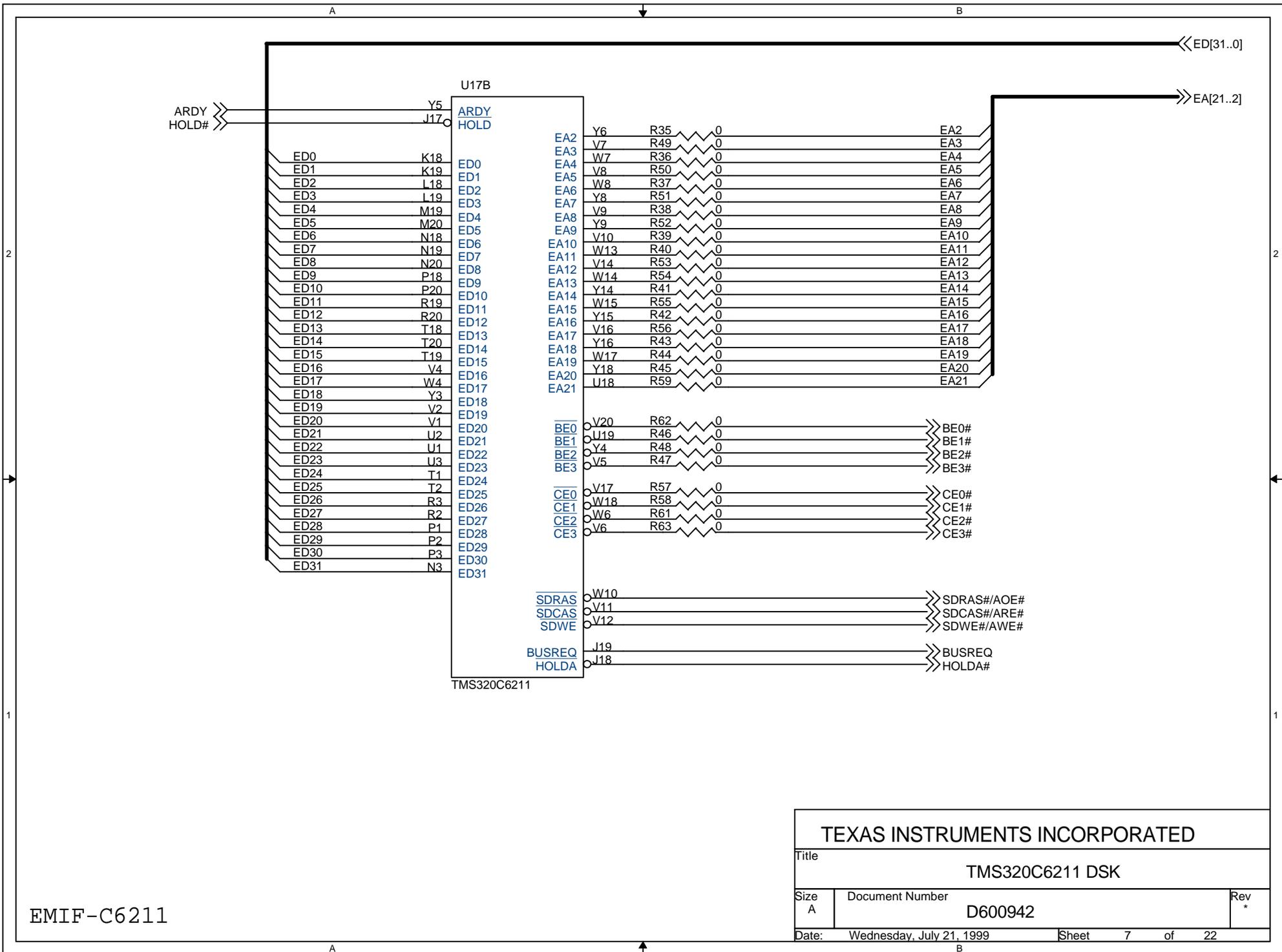


CLOCK-OSCILLATORS

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A

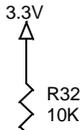
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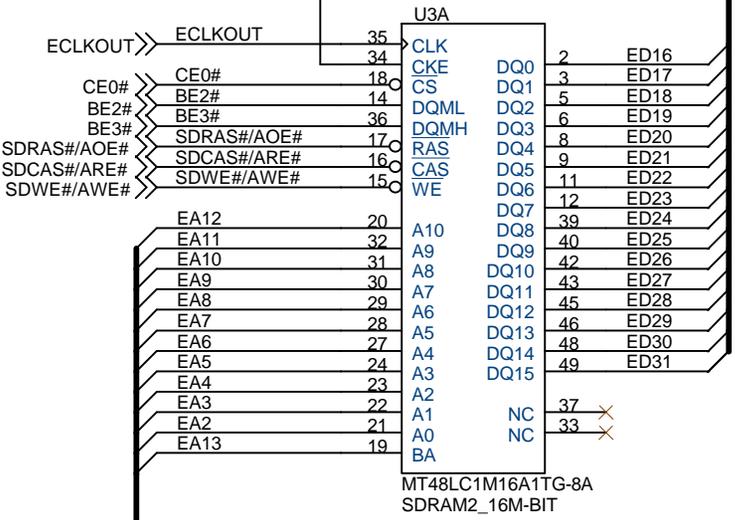
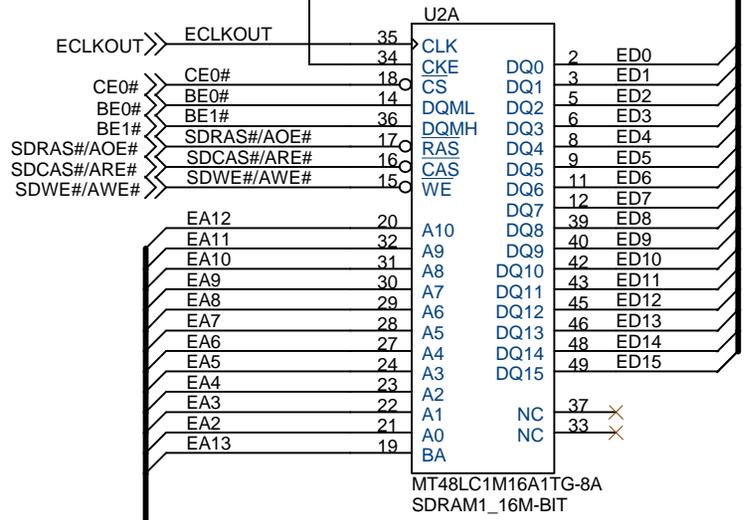
EMIF-C6211

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ED[31..0] <<<



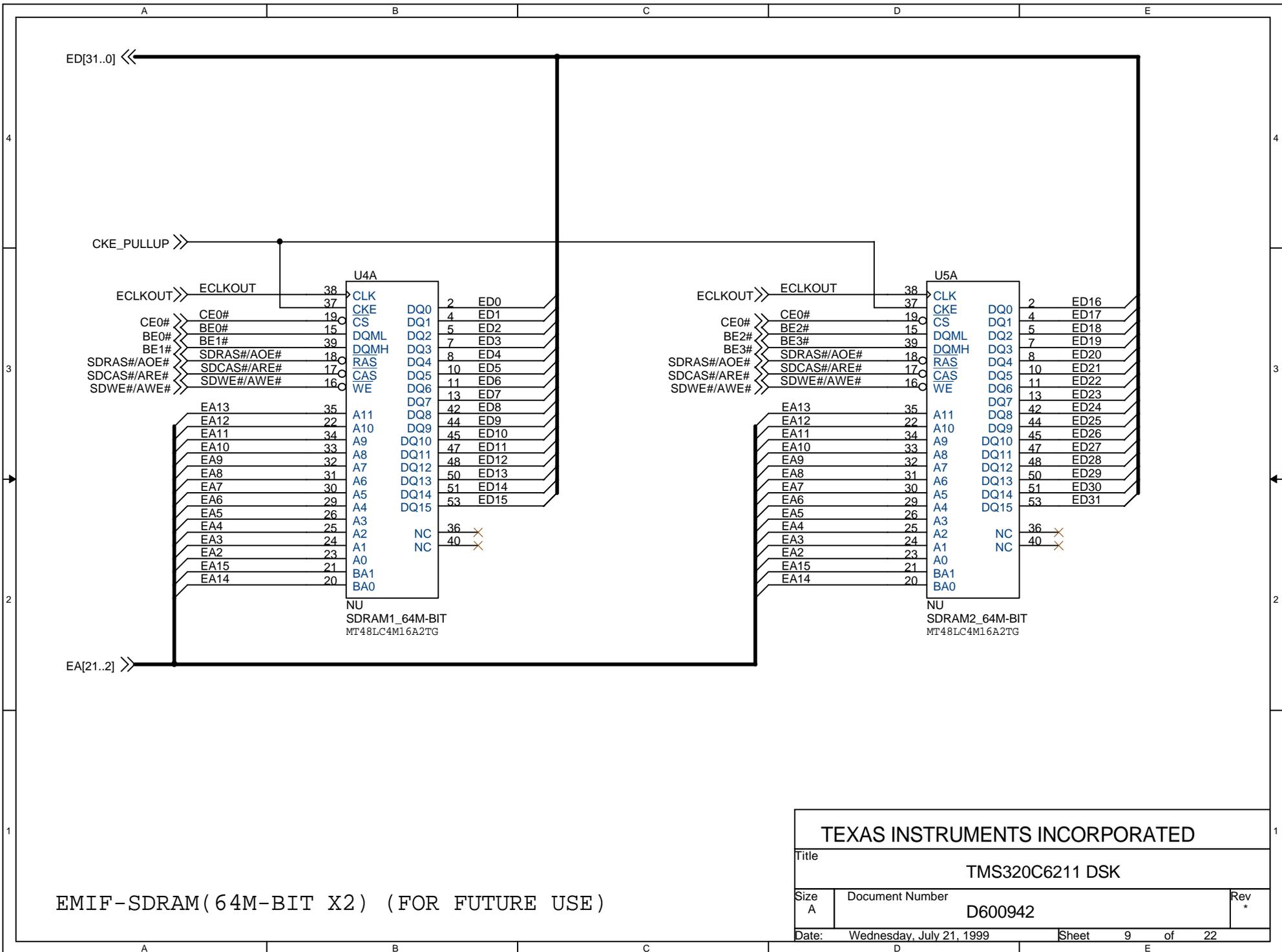
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EA[21..2] >>>

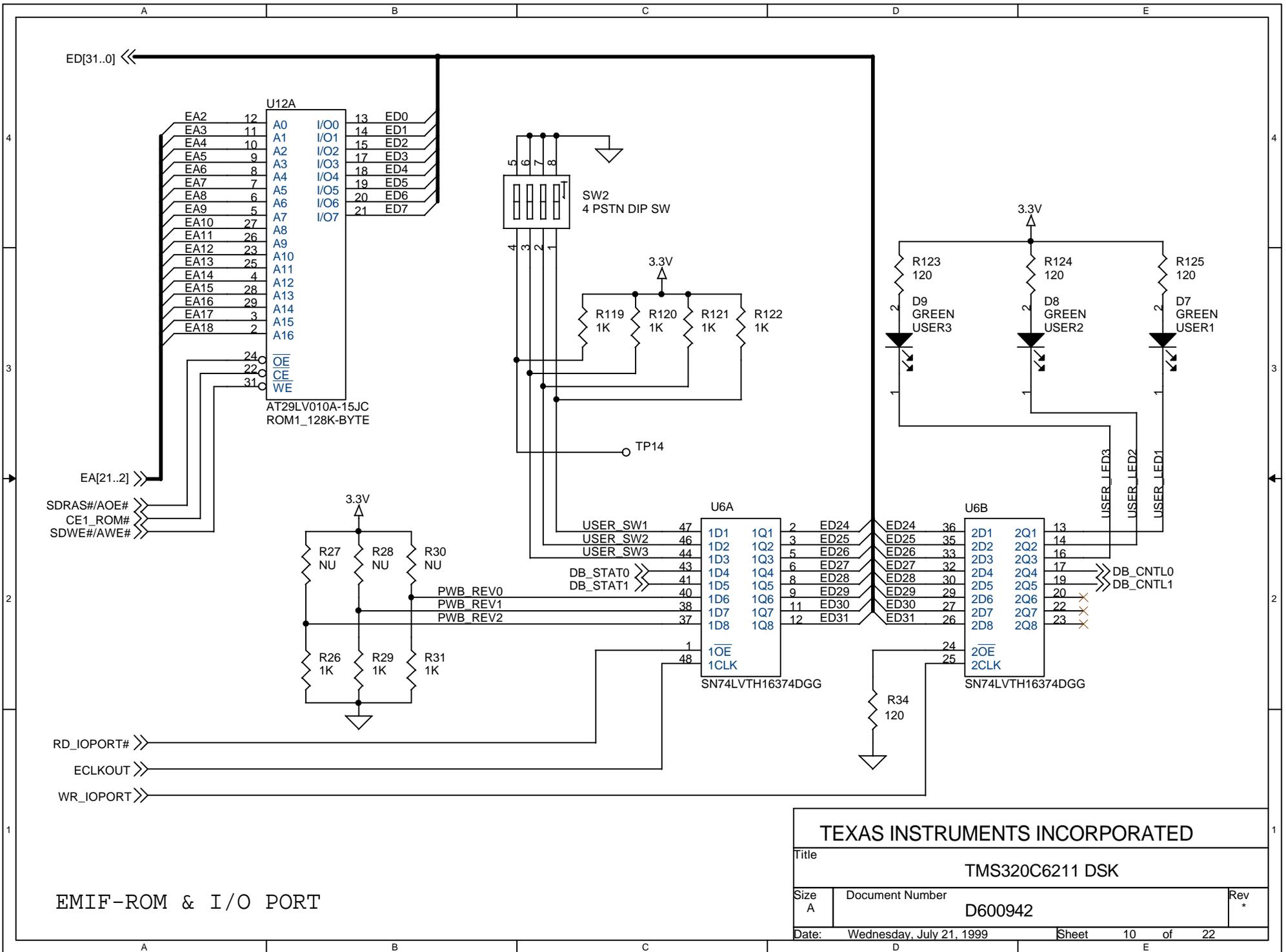
EMIF-SDRAM(16M-BIT X2)

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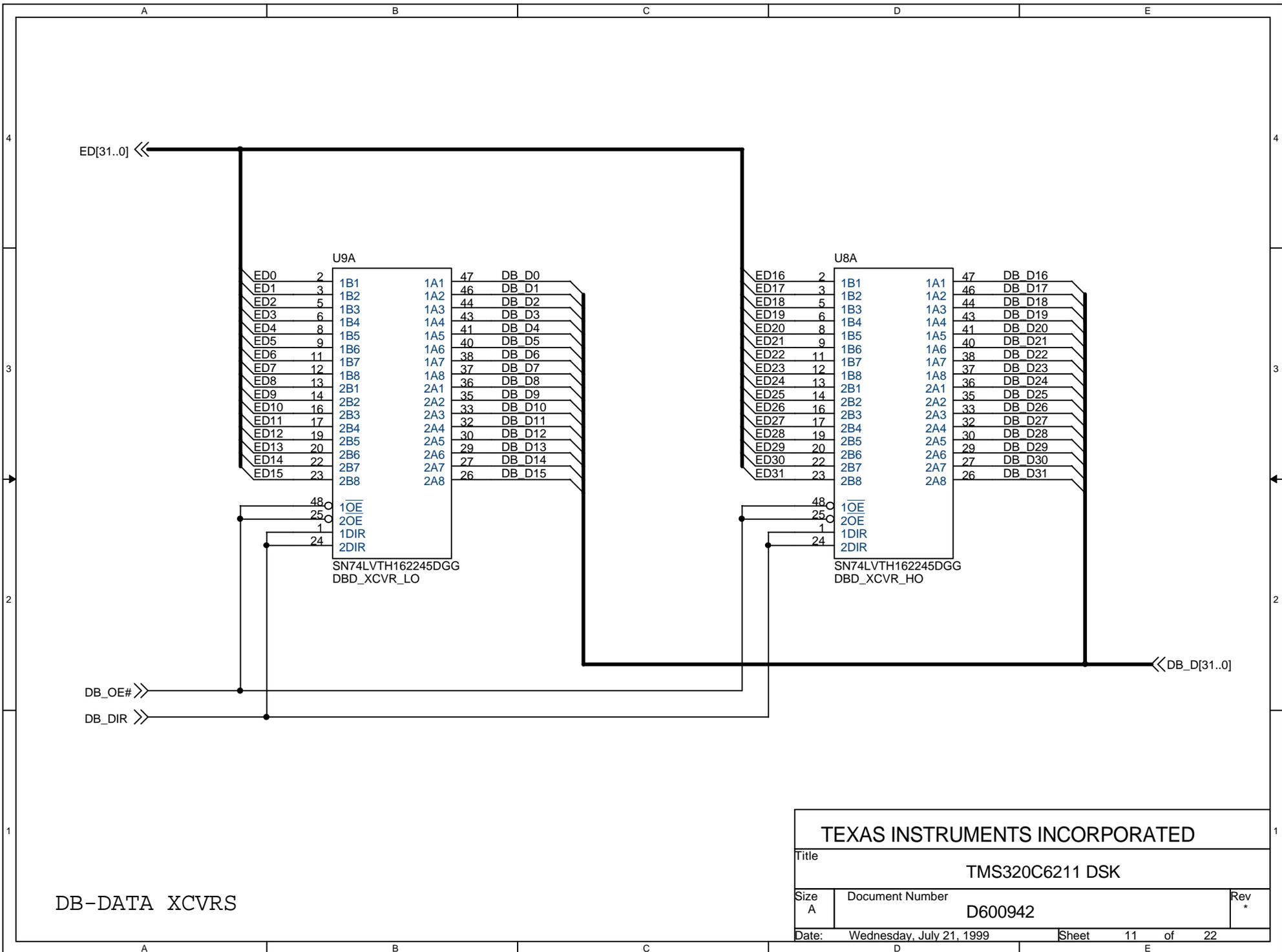
EMIF-SDRAM(64M-BIT X2) (FOR FUTURE USE)

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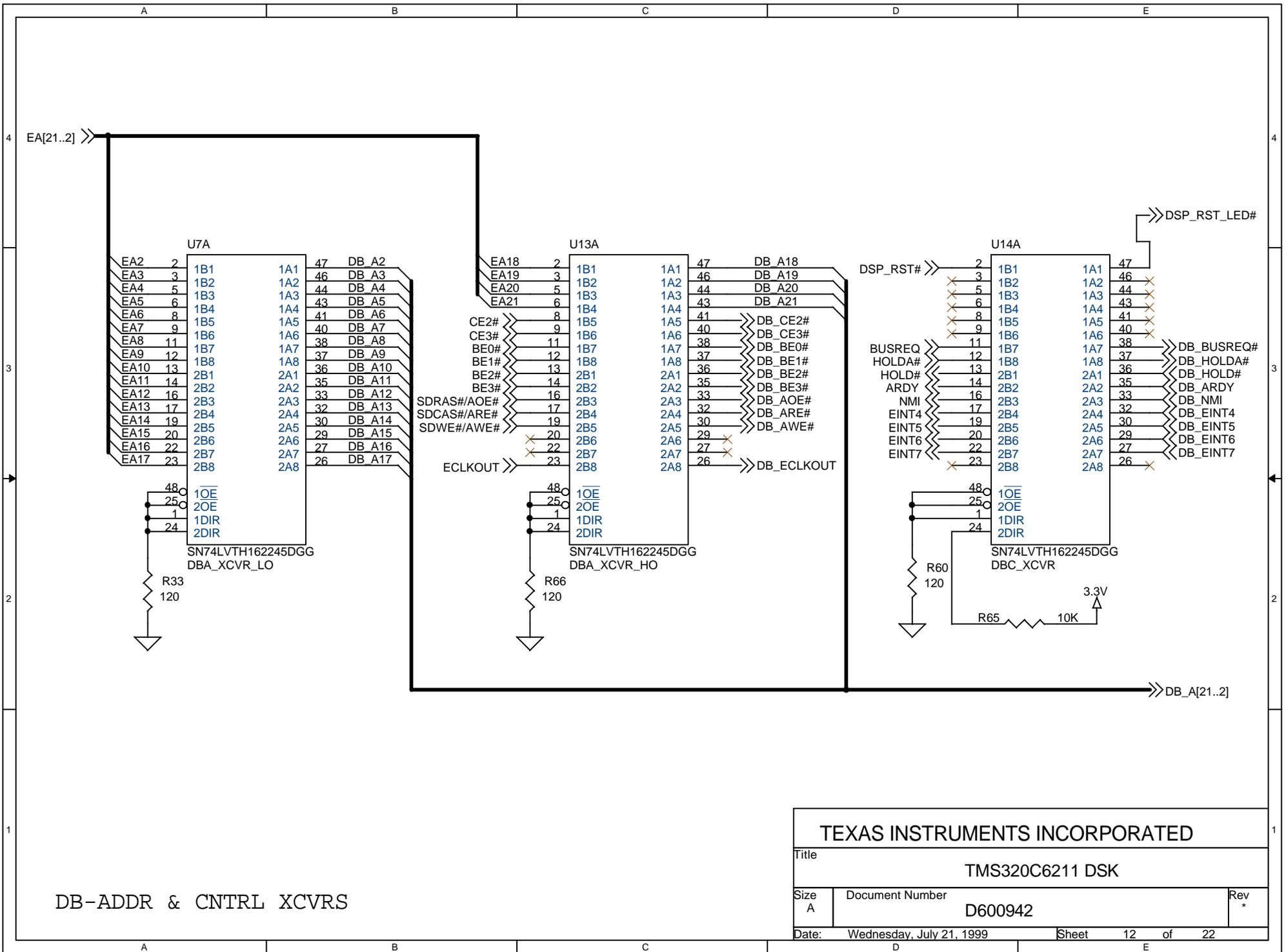
EMIF-ROM & I/O PORT

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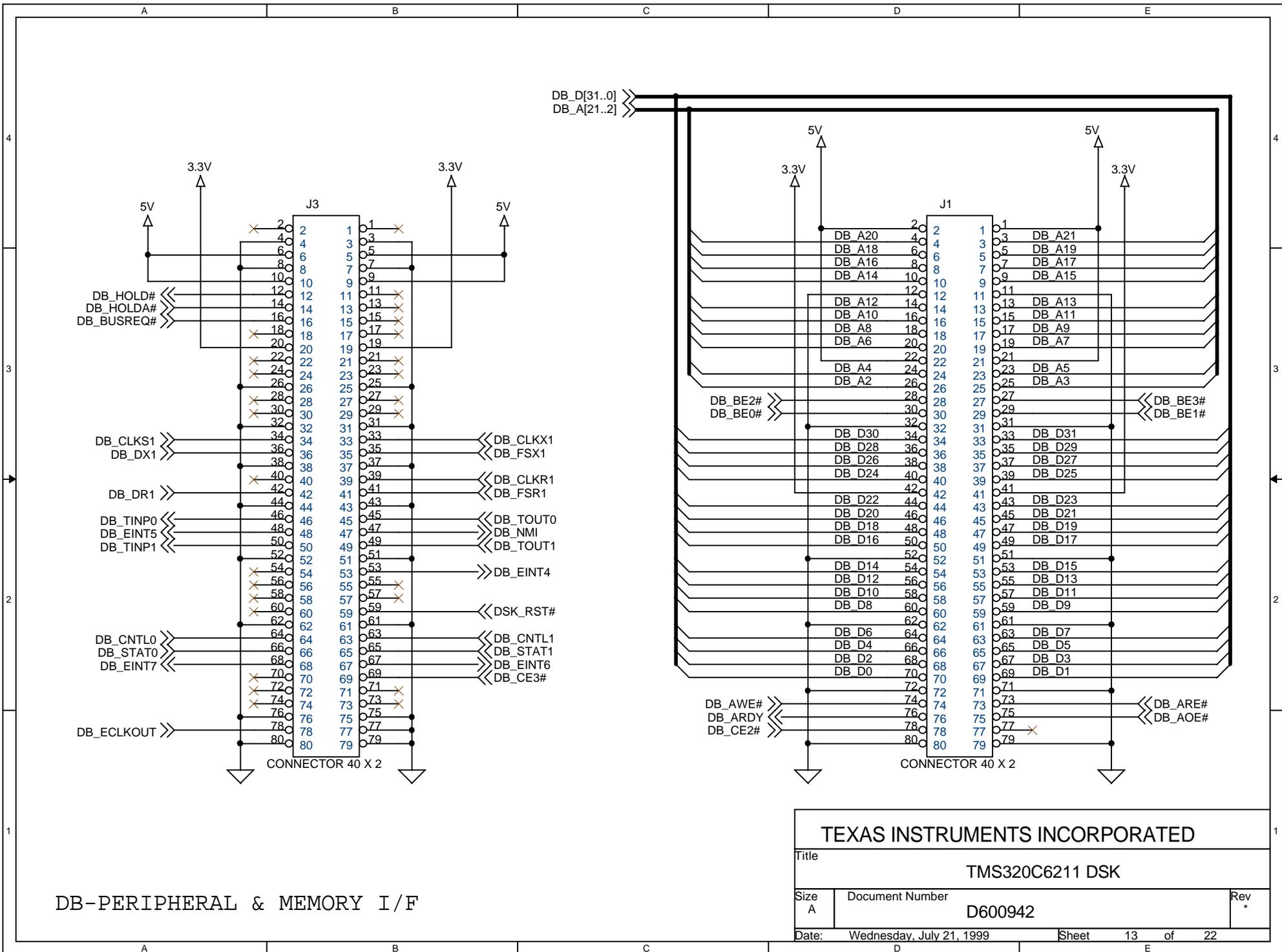
DB-DATA XCVRS

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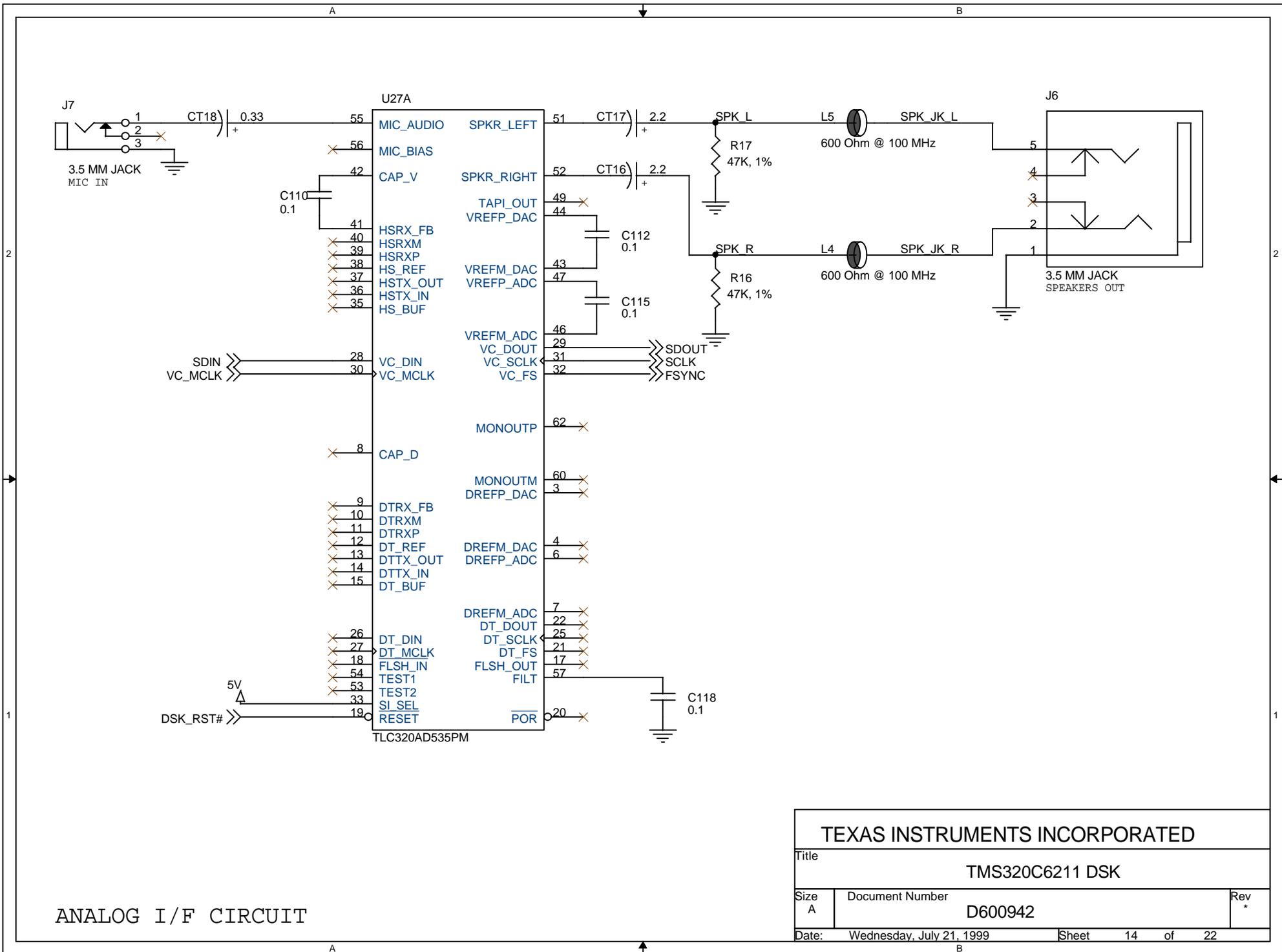
DB-ADDR & CNTRL XCVRs

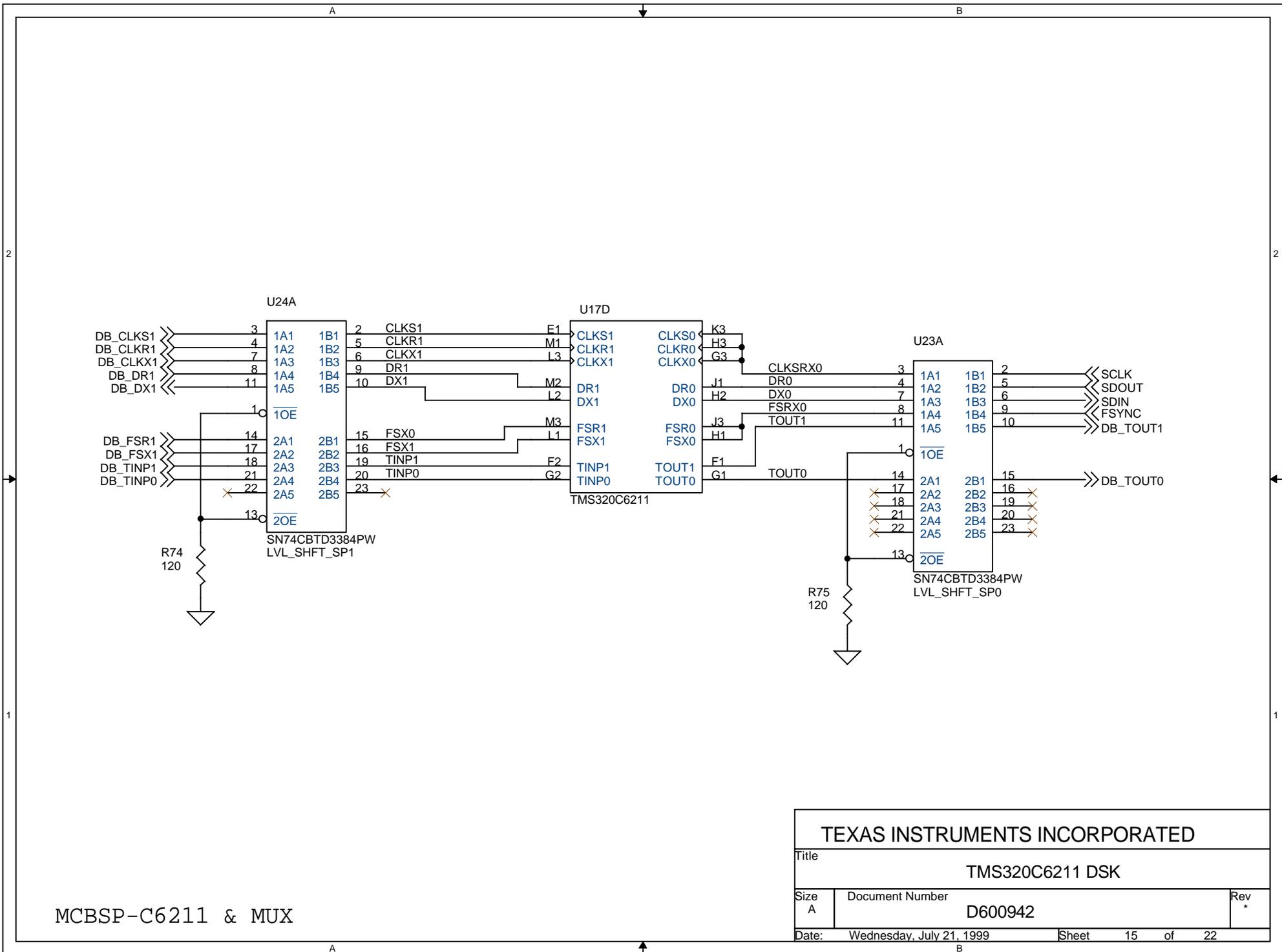
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DB-PERIPHERAL & MEMORY I/F

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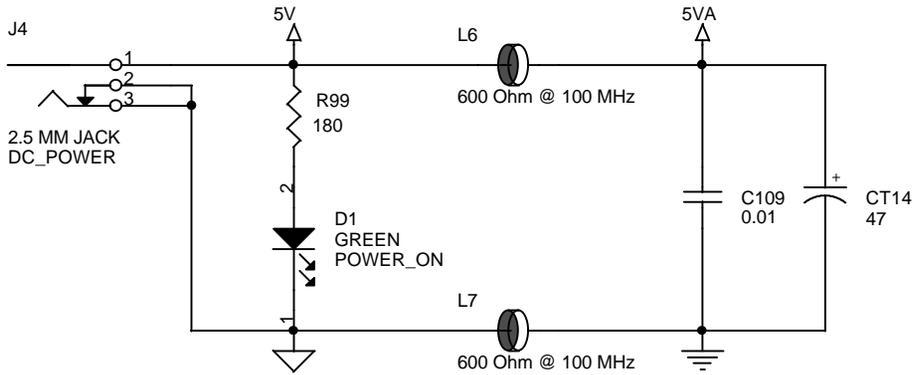




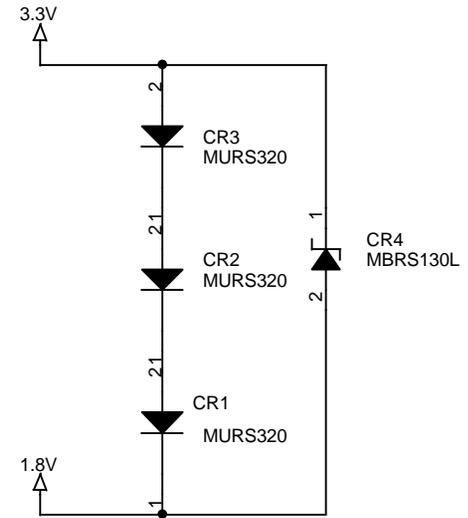
MCBSP-C6211 & MUX

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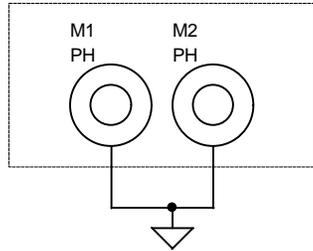
POWER INPUT



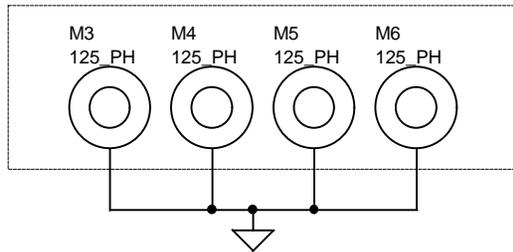
POWER SEQUENCING



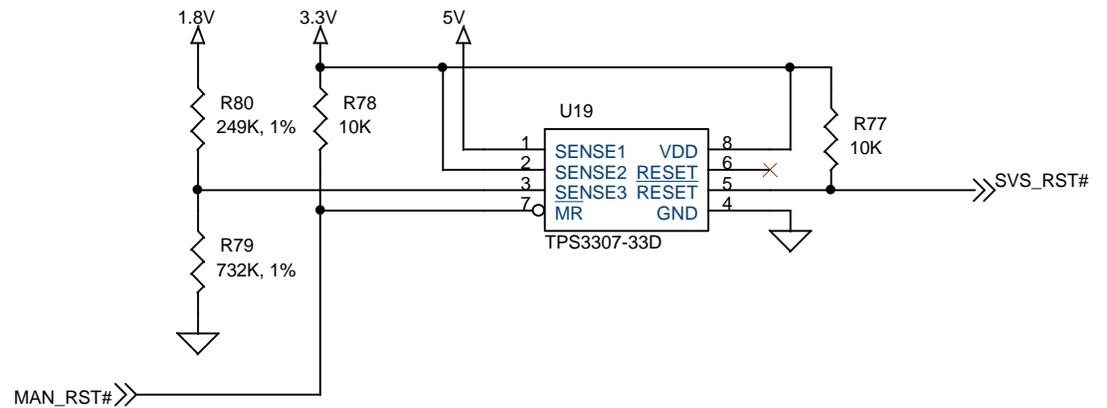
DB-25 CONNECTOR GROUNDING



DAUGHTERBOARD STANDOFF GROUNDING



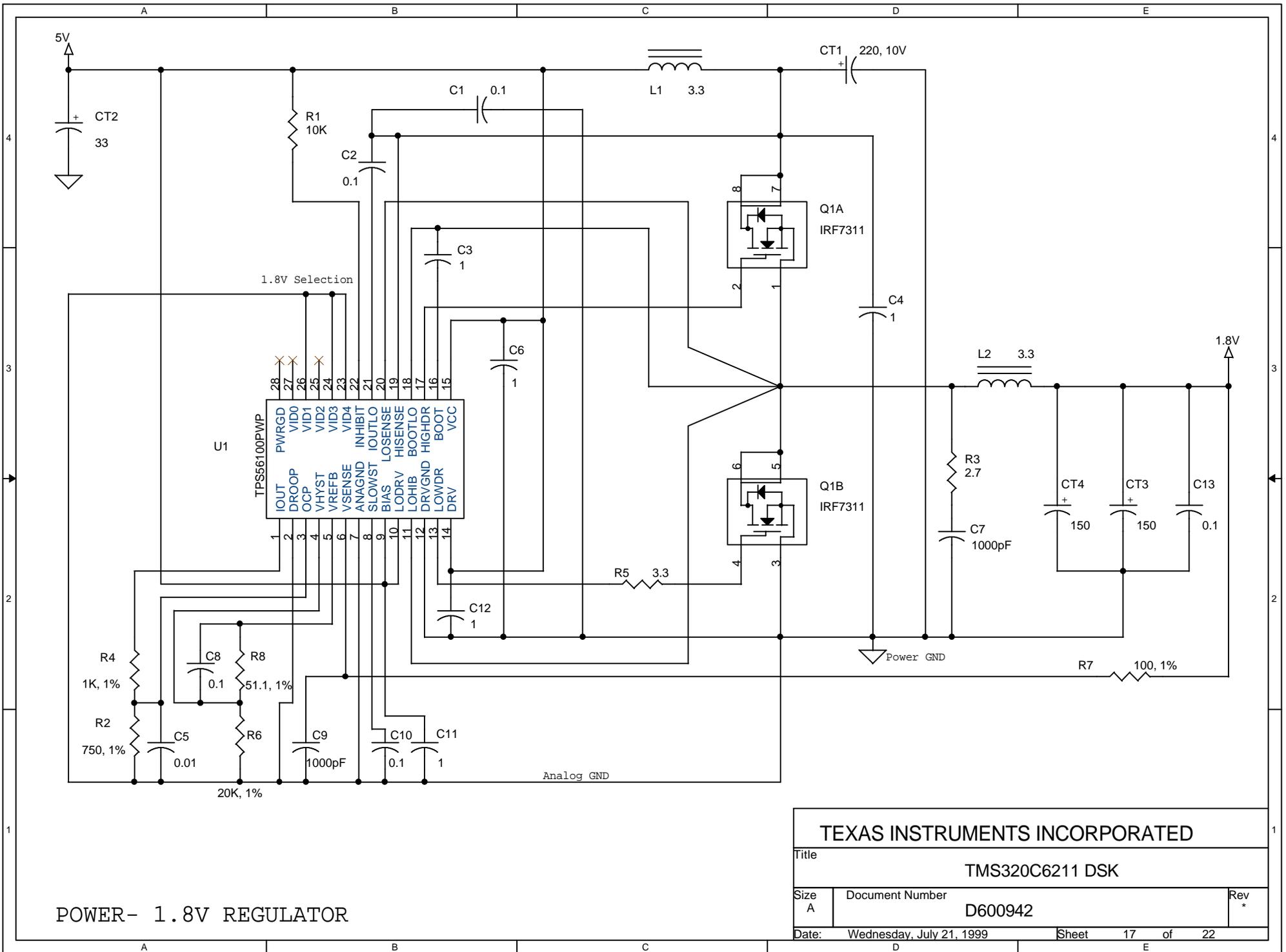
POWER SUPERVISION AND MANUAL RESET



POWER-INPUT, SEQUENCING, & SUPERVISION

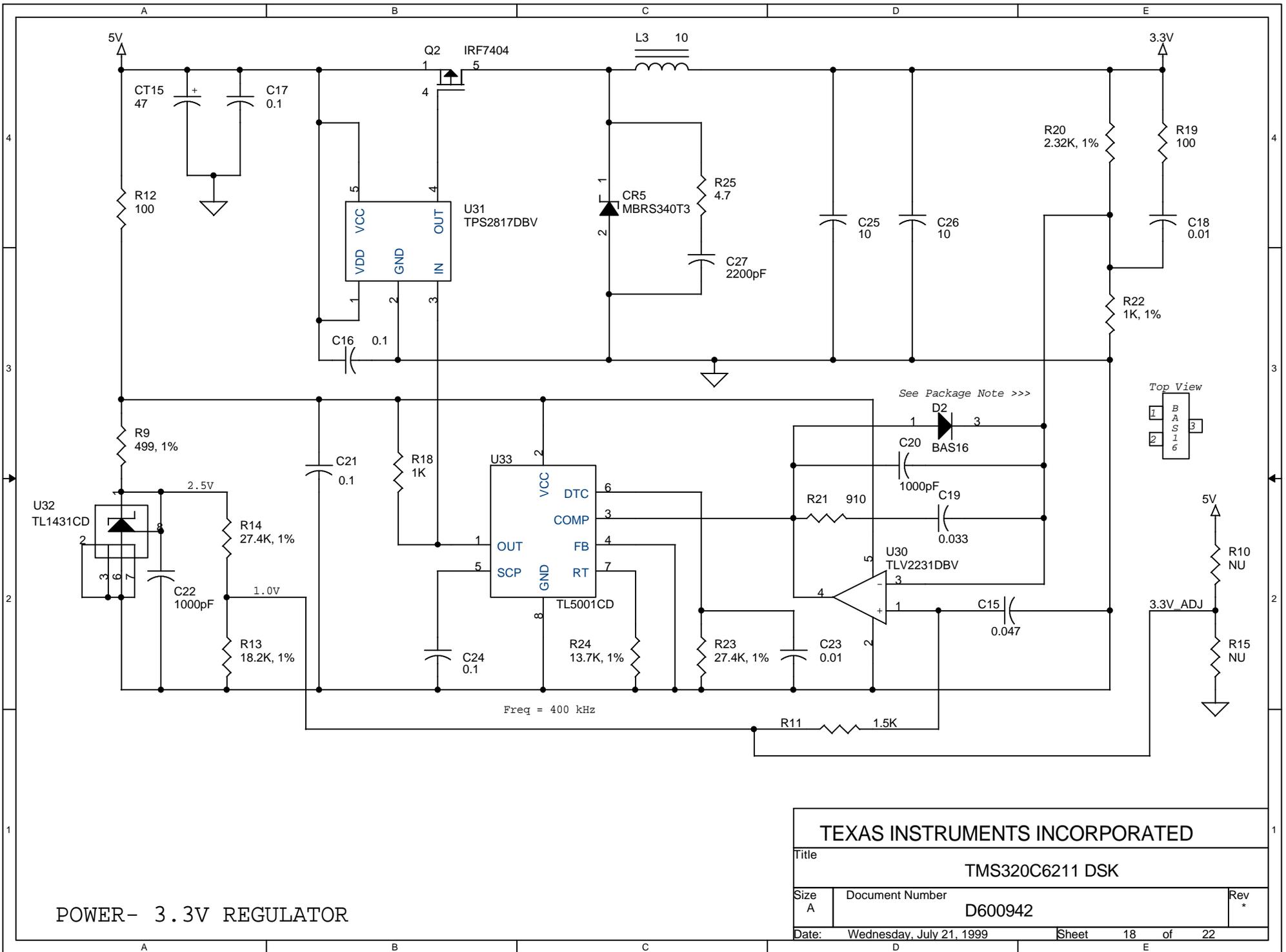
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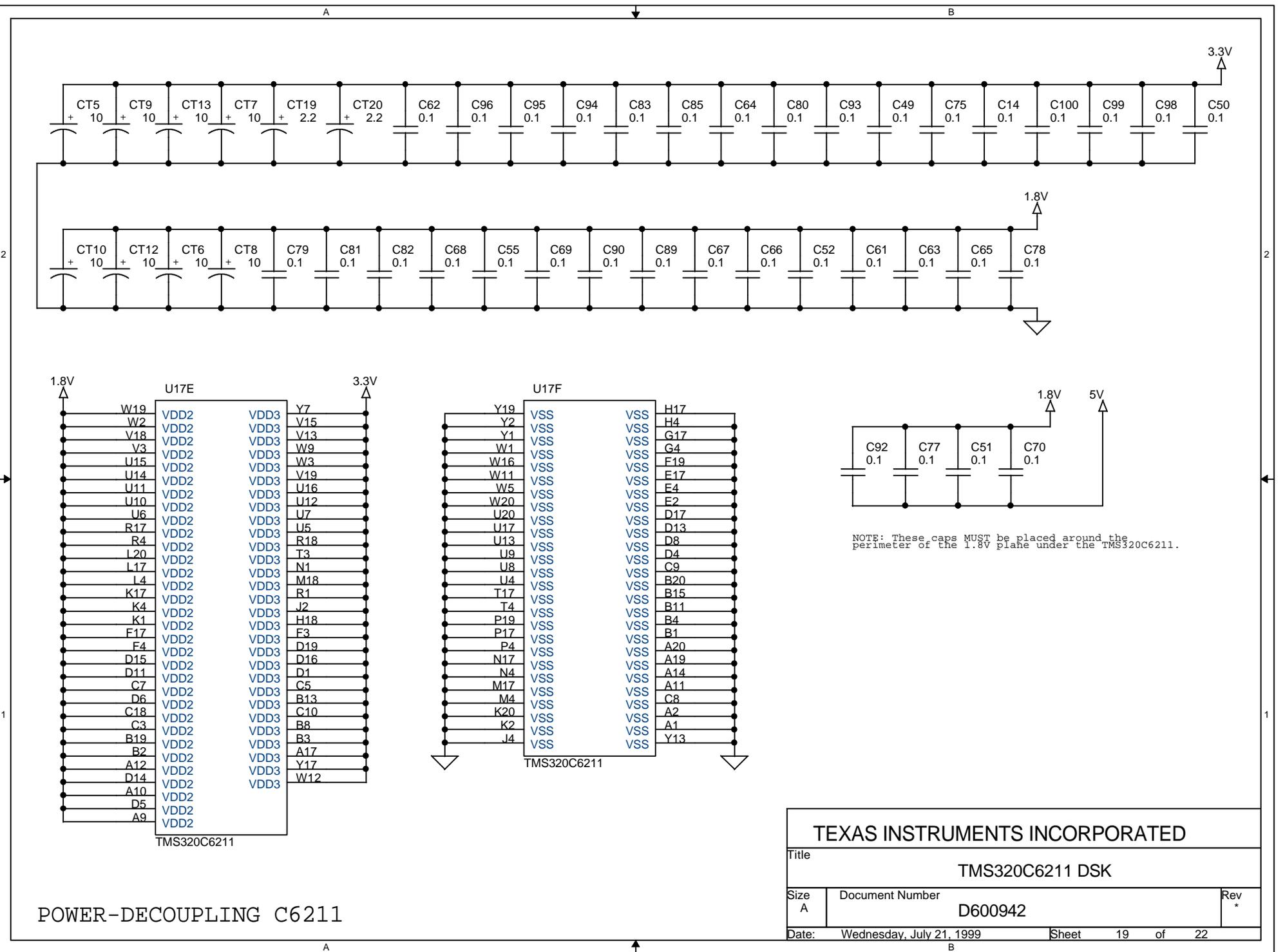
POWER- 1.8V REGULATOR

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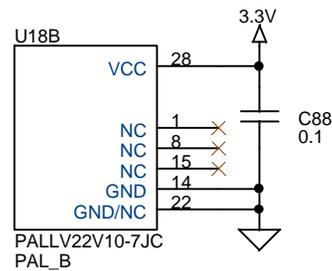
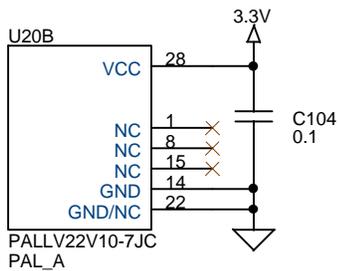
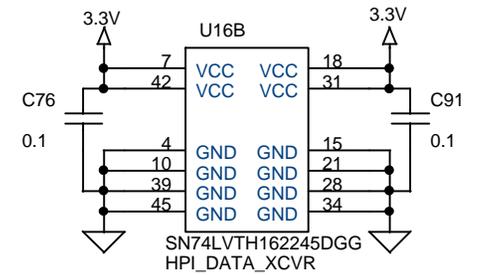
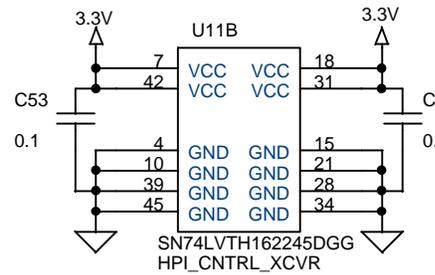
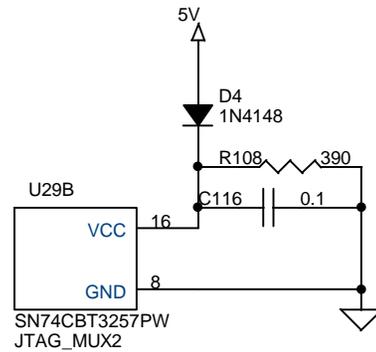
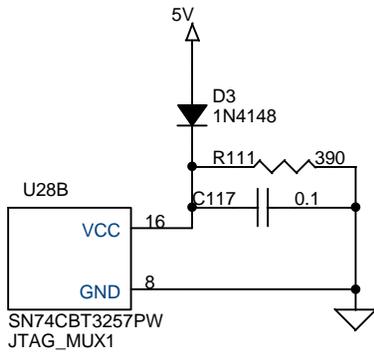
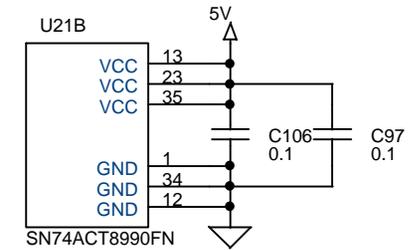
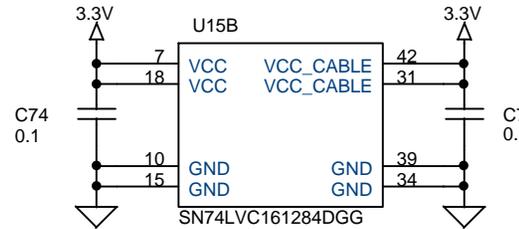
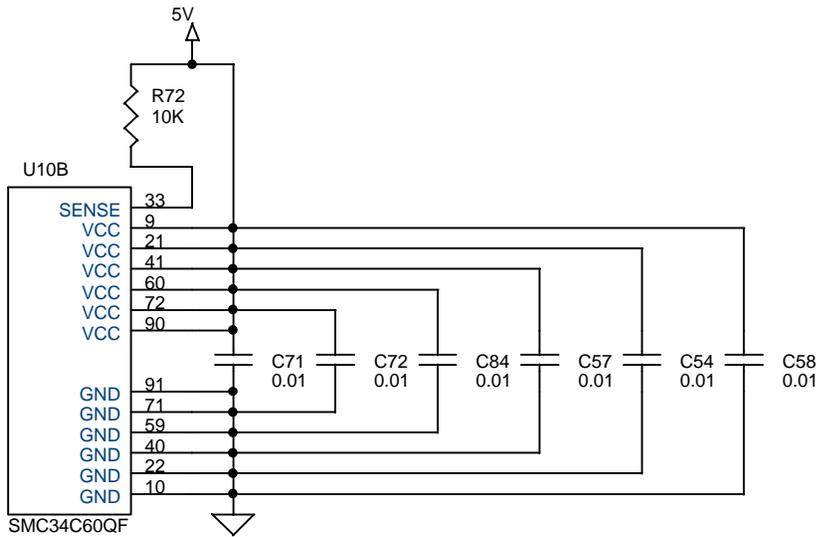
POWER- 3.3V REGULATOR

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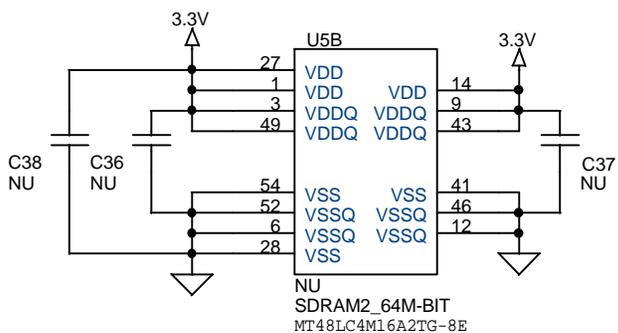
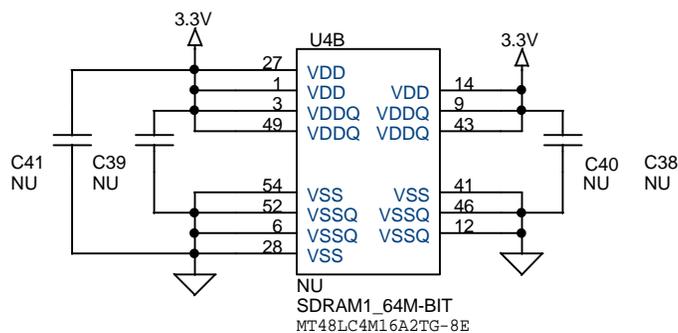
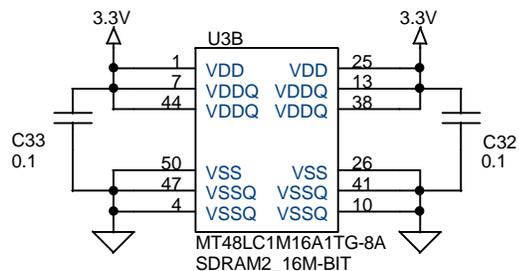
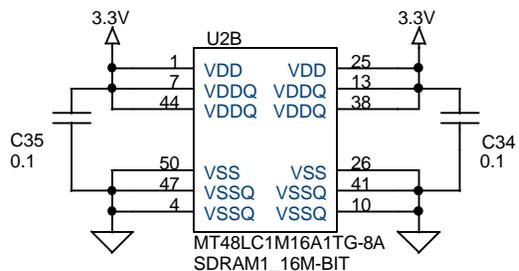
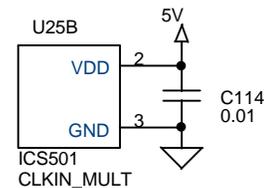
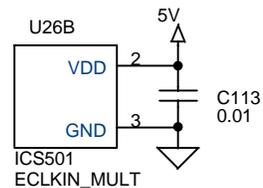
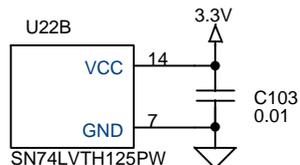
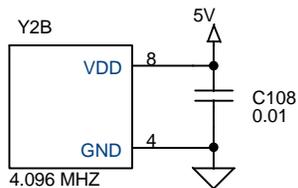
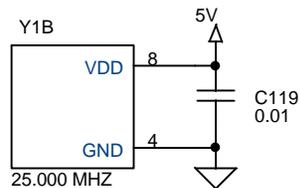


POWER-DECOUPLING C6211

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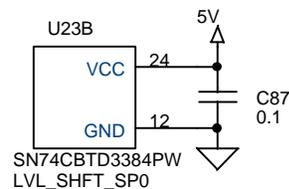
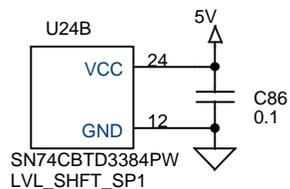
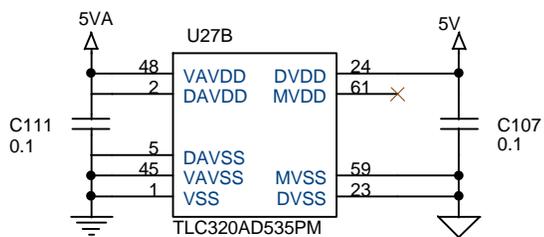
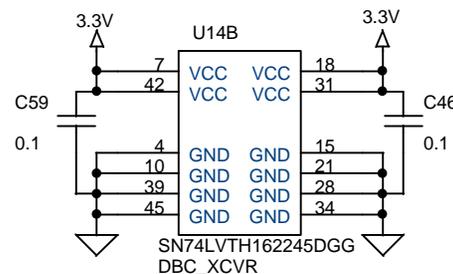
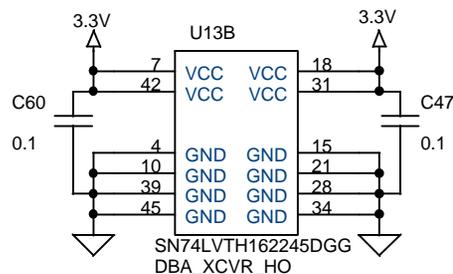
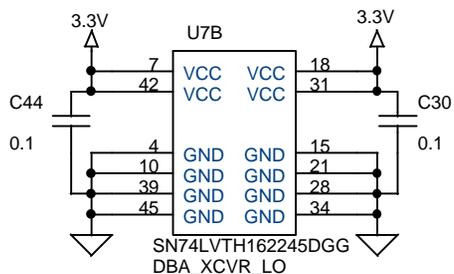
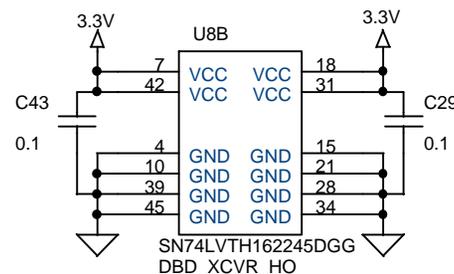
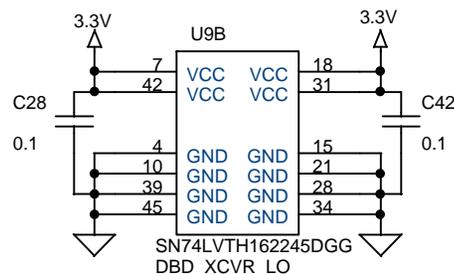
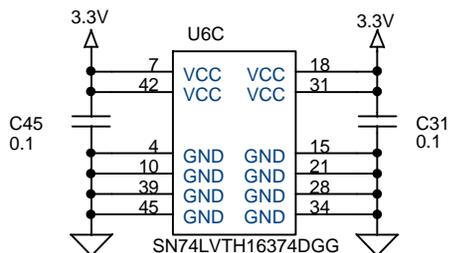
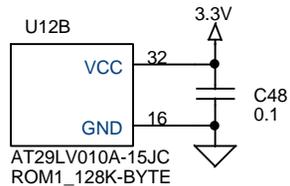


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POWER-DECOUPLING (SHEETS 6-9)

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POWER-DECOUPLING (SHEETS 10-16)

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